

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 04/14/04
Application Type:: Regular
Subject Matter:: Utility
Title:: Bond And Back Side Etchback
Transistor Fabrication Process
Attorney Docket Number:: X-1322-1-1D US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 7
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Robert
Middle Name:: O.
Family Name:: Conn
Street:: 15210 Old Ranch Road
City:: Los Gatos
State or Province:: CA
Postal or Zip Code:: 95033

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/407,746	04/04/03

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124